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From:	Ira Matsil	Date:	June 8, 2004
		Pages:	5
Applicant:	Cho, et al.	Docket No:	TI-36298
Serial No.:	10/786,966	Art Unit:	2829
Filed:	February 25, 2004	Examiner:	Unknown
For:	Built-In Self Test Method and Apparatus for litter Transfer Litter Tolerance as		

FIFO Data Buffer

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Cho, et al.

Docket No.:

TI-36298

Serial No.:

10/786,966

Art Unit:

2829

Filed:

February 25, 2004

Examiner:

Unknown

Title:

Built-In Self Test Method and Apparatus for Jitter Transfer, Jitter Tolerance,

and FIFO Data Buffer

Office of Initial Patent Examination Filing Receipt Corrections Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REQUEST FOR CORRECTION

Sir:

Petitioner hereby requests a correction in the Filing Receipt of the above-referenced patent application. Enclosed is a copy of the Filing Receipt with the changes noted thereon as well as a copy of the signed Declaration showing the correct name of Mr. Bhakta.

Respectfully submitted,

June 8, 2004

Date

Ira S. Matsil

Reg. No. 35,272

Attorney for Applicant

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FILING OR 371 APPL NO. ART UNIT FIL FEE REC'D ATTY. DOCKET NO (c) DATE DRAWINGS **TOT CLMS** 10/786.966 02/25/2004 2829

TI-36298

CONFIRMATION NO. 4127

43

IND CLMS

14

FILING RECEIPT

OC000000012696184

Date Mailed: 05/18/2004

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Applicant(s)

James B. Cho, Plano, TX: - Bhakta Bhavesh; Richardson, TX: Bhavesh G. Bhakta

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 05/17/2004

Projected Publication Date: 08/25/2005

Non-Publication Request: No

Early Publication Request: No

Title

Built-in self test method and apparatus for jitter transfer, jitter tolerance, and FIFO data buffer

Preliminary Class

324

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